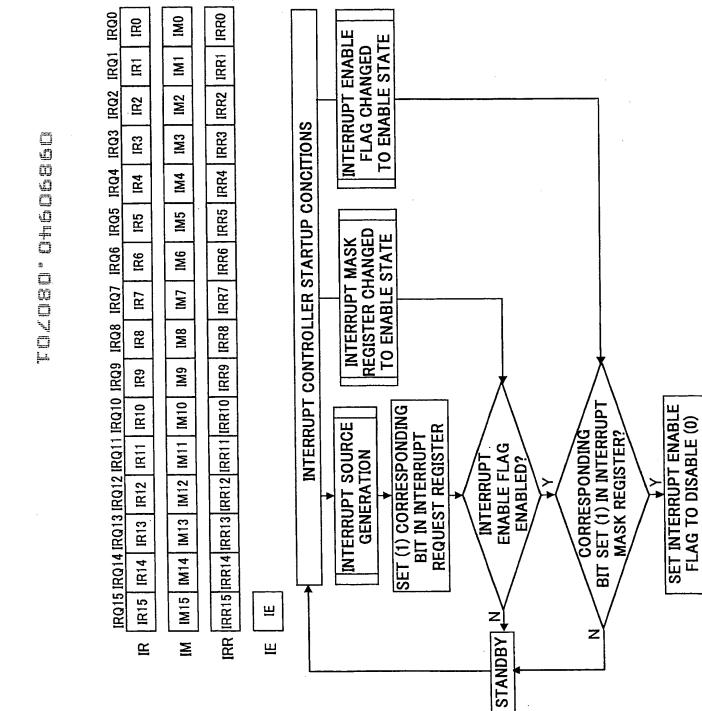


FIG. 1

SHIFT PROCESSING TO INTERRUPT HANDLER



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IRQ0	0	0	0	0	-	-	-	-	-		-		-	-	-	
IRQ1	0	0	0	0	-	-		-		-			-	-		
IRQ2	0	0		-				-	-			-	-	0	-	-
IRQ3	0	0					-	-	-	-				-	-	-
IRQ4 IF	0	0	0	0	0	0		0	0			-		-		-
IRQ5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
IRQ6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
IRQ7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
IRQ8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
IRQ9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
IRQ10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
IRQ11	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
IRQ15 IRQ14 IRQ13 IRQ12 IRQ11 IRQ10	0	0	0	0	0	0	0	0	0							
IRQ13	0	0	0	0	0	0	0	0	0	-	-	-	-		-	
IRQ14	0	0	0	0	0	0	0	0	0	-	-	-	-		-	
IRQ15	0	0	0	0	0	0	0	0	0	-	-			-		-
	IMFG2	IMFG3	IMFG0	IMFG1	IMFG15	IMFG14	IMFG13	IMFG12	IMFG4	IMFG5	IMFG6	IMFG7	IMFG8	IMFG9	IMFG10	IMFG11
		LEVEL1							LEVEL3							

FIG.3

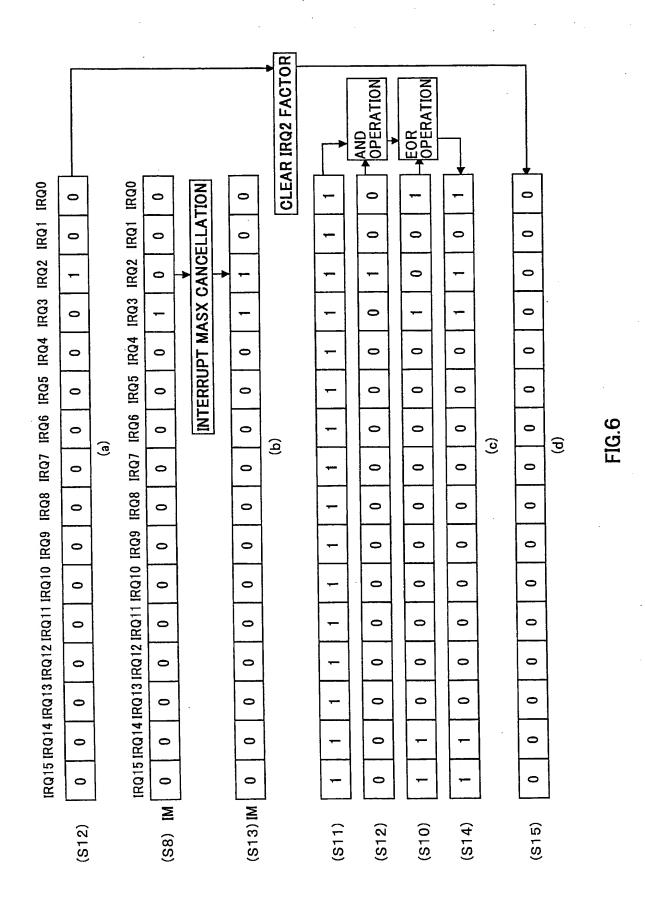
4/10														
,			OPERATION	 -			OPERATION] 	<u> </u>		OPERATION] -		7
IRGO	-		-		_		<u>-</u>		-		0		0	
ואמו			-		-		- .		-		0		0	
IKQ2	0		0		0		-		0				0	
IRQ3	0				0		1		0		-		0	$\left \right $
IRQ4	0		0		0		0		0		0		0	1
IRQ5	0		0		0		0		0		0		0	1
RO11 IRO10 IRO9 IRO8 IRO7 IRO6 IRO5 IRO4 IRO3 IRO2 IRO1	0		0		0		0		0		0		0	1
IRO7	0		0		0		0		0		0		0	
IRQ8	0		0		0		0		0		0		0	
IRQ9	0		0		0		0		0		0		0	
IRQ10	0		0		0		0		0		0		0	
IRQ11	0		0		0		0		0		0		0	
IRQ12	_		0		0		0		0		0		0	
IRQ 13	0		0		0		0		0		0		0	
IRQ14	_		-		-		0		0		0		0	
IRQ15 IRQ14 IRQ13 IRQ12 II	0		-		0		0		0		0		0	
	(S1) IR1	-	(S2) IM	•	(83)		(S4)IMFG14 0		(32)		(S6) IMFG1	-	(87)	

FIG.

			5	/10	•		
		AND			EOR		
IRQ0	0	_	0	-	0	-	_
IRQ1	0	-	0	-		0	-
IRQ2	-	0	0	0	0	0	-
IRQ3	-	-			0	-	-
IRQ4	0	0	0	0	0	0	1
IRQ5	0	0	0	0	0	0	-
IRQ11 IRQ10 IRQ9 IRQ8 IRQ7 IRQ6 IRQ5 IRQ4 IRQ3 IRQ2 IRQ1 IRQ0	0	0	0	0	0	0	-
IRQ7	0	0	0	0	0	0	-
IRQ8	0	0	0	0	0	0	-
IRQ9	0	0	0	0	0	0	-
IRO10	0	0	0	0	0	0	-
IRQ11	0	0	0	0	0	0	_
IRQ12	0	0	0	0	0	0	-
IRQ15 IRQ14 IRQ13 IRQ12	0	0	0	0	0	0	
IRQ14	0	-	0	-	0	_	-
IRQ15	0		0	-	0	_	_
	(S6)IMFG1	(S2) IM	(88)	(S2) IM	(68)	(S10)	(\$11)

FIG





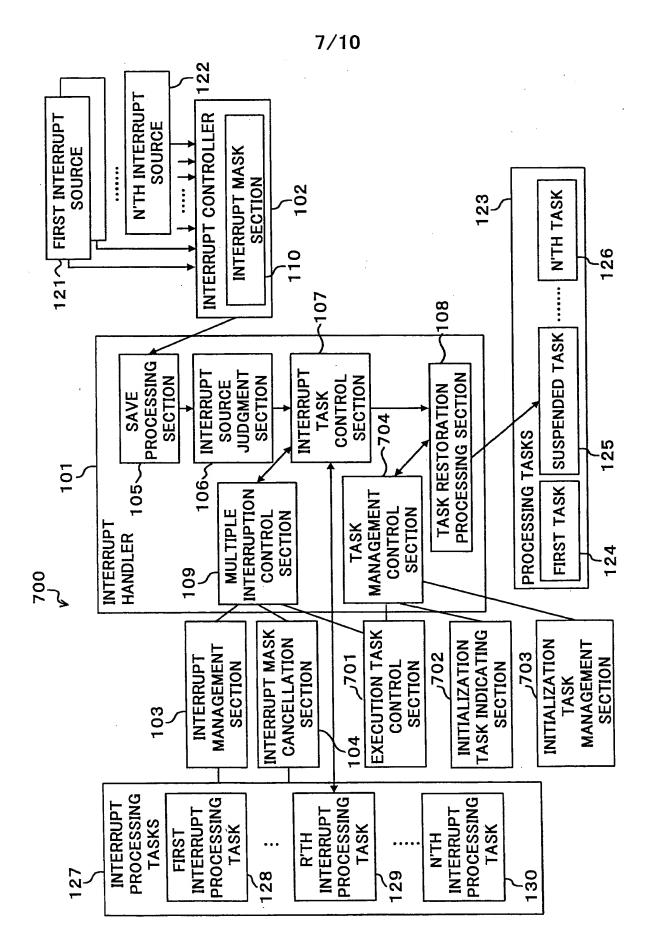


FIG.7

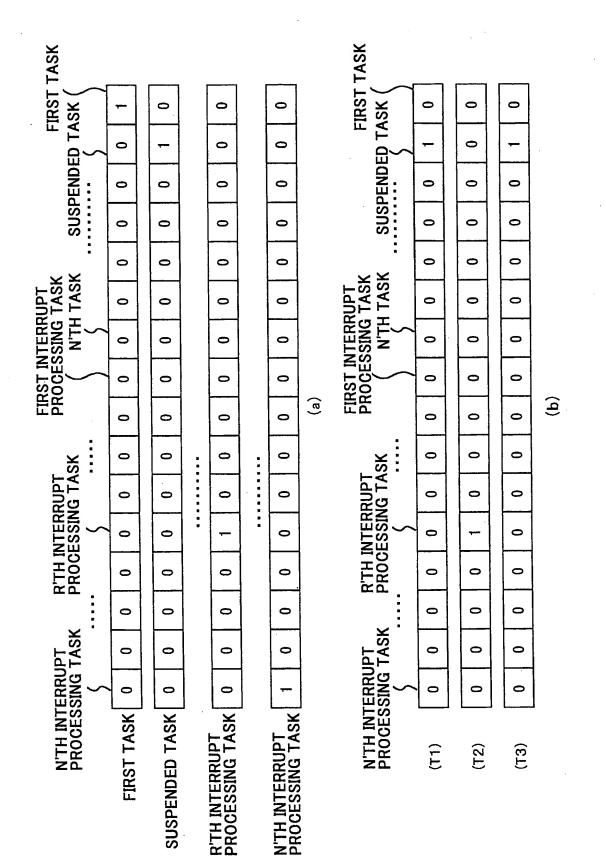


FIG.8

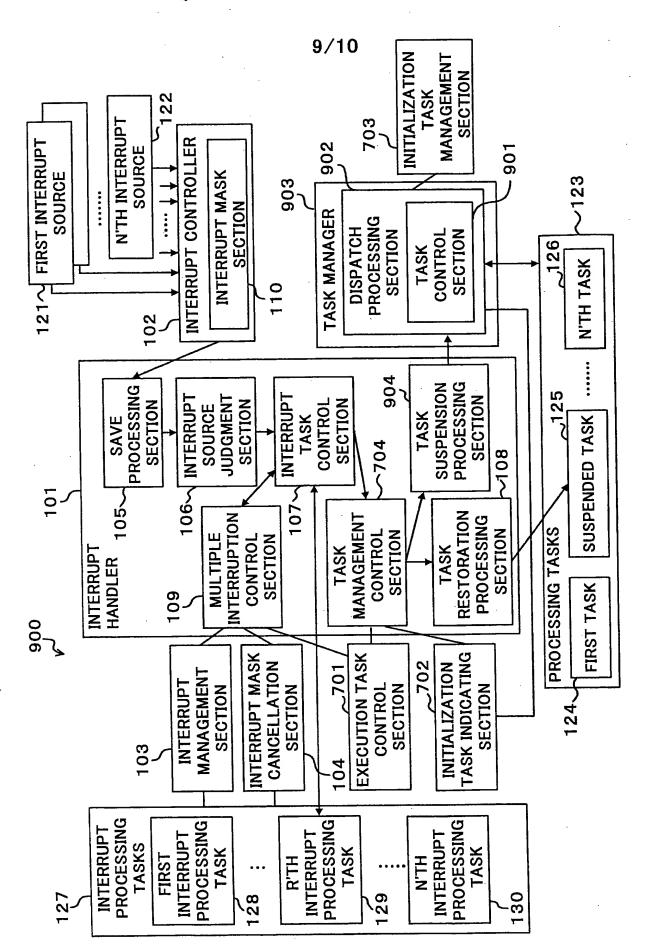


FIG.9

